Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	neutral near2 instruction and g06f$.ipc.	EPO; JPO	OR	ON	2004/11/03 17:09
L2	5	neutral near2 instruction and g06f$.ipc.	DERWENT; IBM_TDB	OR	ON	2004/11/03 17:10
L3	0	neutral near2 instruction and g06f$.ipc.	USOCR	OR	ON	2004/11/03 17:10
S1	2	pipeline with stall with (neutral NOP instruction) with test$3	US-PGPUB; USPAT	OR	ON	2004/02/20 13:29
S2	12	stall with (neutral NOP instruction) with test$3	US-PGPUB; USPAT	OR	ON	2004/02/18 09:52
S3	1	pipeline with stall with (neutral)	US-PGPUB; USPAT	OR	ON	2004/02/18 09:55
S4	32	smith-ronal$.in. and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2004/02/18 09:54
S5	42	pipeline with stall with instruction with (test$3 verif$7 check$3 analy$4 examin$3)	US-PGPUB; USPAT	OR	ON	2004/02/18 10:02
S6	1	pipeline with stall with (insert$3 generat$3 execut$3) with NOP with (test$3 verif$7 check$3 analy$4 examin$3)	US-PGPUB; USPAT	OR	ON	2004/02/18 10:03
S7	1	pipeline with stall with NOP with (test$3 verif$7 check$3 analy$4 examin$3)	US-PGPUB; USPAT	OR	ON	2004/02/18 10:04
S8	426	712/227.ccls.	US-PGPUB; USPAT	OR	ON	2004/02/18 10:14
S9	1	test$3 near3 processor with NOP	US-PGPUB; USPAT	OR	ON	2004/02/18 10:15
S10	247	test$3 near3 processor with execut$3 with instruction	US-PGPUB; USPAT	OR	ON	2004/02/18 10:16
S11	1	test$3 near3 processor with execut$3 with instruction same stall	US-PGPUB; USPAT	OR	ON	2004/02/18 10:35
S12	13	"neutral instruction"	US-PGPUB; USPAT	OR	ON	2004/02/18 10:47
S13	1	(pseudo adj random adj generator) with NOP	US-PGPUB; USPAT	OR	ON	2004/02/18 10:50
S14	77	architectural near2 (comparison verification)	US-PGPUB; USPAT	OR	ON	2004/02/18 11:02
S15	1	test$3 with resource with (idle near3 (processor pipeline))	US-PGPUB; USPAT	OR	ON	2004/02/19 08:53
S16	1	(test$3 near3 register) with pipeline with stall	US-PGPUB; USPAT	OR	ON	2004/02/18 13:13

S17	25	(test$3 near3 register) with stall	US-PGPUB; USPAT	OR	ON	2004/02/18 13:14
S18	0	(test$3 near3 (resource hardware)) with stall	US-PGPUB; USPAT	OR	ON	2004/02/18 13:14
S19	0	((verif$4 confirm$3) near3 (resource hardware)) with stall	US-PGPUB; USPAT	OR	ON	2004/02/18 13:15
S20	11	("5331571" | "5737516" | "5828824" | "5970241" | "6032268" | "6055649" | "6065106" | "6081885" | "6085336" | "6112298" | "6141636").PN.	USPAT	OR	OFF	2004/02/19 08:50
S21	2	(test$3 near3 register) with ((idle stall$3) near3 (processor pipeline))	US-PGPUB; USPAT	OR	ON	2004/02/19 08:56
S22	7	((error examin$3 check$3 verif$4) near3 register) with ((idle stall$3) near3 (processor pipeline))	US-PGPUB; USPAT	OR	ON	2004/02/19 08:58
S23	183	714/35.ccls.	US-PGPUB; USPAT	OR	ON	2004/02/19 08:58
S24	5	714/35.ccls. and (stall near3 pipeline)	US-PGPUB; USPAT	OR	ON	2004/02/19 08:59
S25	13	714/35.ccls. and idle	US-PGPUB; USPAT	OR	ON	2004/02/19 08:59
S26	1	"5329471".pn. and (access$3 test$3) with (register resource)	US-PGPUB; USPAT	OR	ON	2004/02/19 09:51
S27	0	compiler with insert$3 with NOP with stall$3	US-PGPUB; USPAT	OR	ON	2004/02/19 11:39
S28	30	compiler with insert$3 with NOP	US-PGPUB; USPAT	OR	ON	2004/02/19 11:40
S29	0	compiler with NOP with schedul$3 with (stall$3 hazard conflict$3)	US-PGPUB; USPAT	OR	ON	2004/02/19 12:49
S30	4	compiler with NOP with schedul$3	US-PGPUB; USPAT	OR	ON	2004/02/19 12:50
S31	3	compiler with NOP with (stall$3 hazard conflict$3)	US-PGPUB; USPAT	OR	ON	2004/02/19 12:50
S32	32	(test$3 verif$4 check$3) with register with (value contents) with (logic$2 near2 operation)	US-PGPUB; USPAT	OR	ON	2004/02/19 16:00
S33	0	register with (logic$2 near2 operation) with (all adj3 (one ones "1's"))	US-PGPUB; USPAT	OR	ON	2004/02/19 16:01
S34	381	register with (logic$2 near2 operation) with "AND"	US-PGPUB; USPAT	OR	ON	2004/02/19 16:03
S35	19	register with (logic$2 near2 operation) with "AND" with mask with ("1" one ones)	US-PGPUB; USPAT	OR	ON	2004/02/19 16:06

S36	0	test$3 with register with (logic$2 near2 operation) with ("AND" and "OR") with mask	US-PGPUB; USPAT	OR	ON	2004/02/19 16:07
S37	5	test$3 with register with (logic$2 near2 operation) with "AND" and "OR" with mask	US-PGPUB; USPAT	OR	ON	2004/02/19 16:07
S38	11	(test$3 verif$4) with register with (logic$2 near2 operation) with ("AND" and "OR")	US-PGPUB; USPAT	OR	ON	2004/02/19 16:08
S39	13	register with (logic$2 near2 operation) with ("AND" and "OR") with mask	US-PGPUB; USPAT	OR	ON	2004/02/19 16:11
S40	6	test$3 with register with ("AND" and "OR") with mask	US-PGPUB; USPAT	OR	ON	2004/02/19 16:32
S41	337	test$3 with register with mask	US-PGPUB; USPAT	OR	ON	2004/02/19 16:35
S42	0	test$3 with register with mask with all with (ones one "1's")	US-PGPUB; USPAT	OR	ON	2004/02/19 16:36
S43	88	test$3 with register with mask with (ones one "1's")	US-PGPUB; USPAT	OR	ON	2004/02/19 16:37
S44	6	test$3 with register with mask with ("AND" and "OR")	US-PGPUB; USPAT	OR	ON	2004/02/20 08:51
S45	2	register with mask with ("AND" and "OR") with ("FF" "00")	US-PGPUB; USPAT	OR	ON	2004/02/20 08:52
S46	119	register with mask with ("AND" and "OR") with (ones one "1")	US-PGPUB; USPAT	OR	ON	2004/02/20 08:57
S47	20	mask with "AND" with ("11111111" "FF")	US-PGPUB; USPAT	OR	ON	2004/02/20 09:01
S48	3545	mask with "AND"	US-PGPUB; USPAT	OR	ON	2004/02/20 09:01
S49	97	mask with "AND" with test$3	US-PGPUB; USPAT	OR	ON	2004/02/20 09:01
S50	29	"AND" near3 NOP	US-PGPUB; USPAT	OR	ON	2004/02/20 09:45
S51	102	"AND" near3 neutral	US-PGPUB; USPAT	OR	ON	2004/02/20 09:45
S52	3	logic$2 with "AND" near3 neutral	US-PGPUB; USPAT	OR	ON	2004/02/20 09:45
S53	6	(logic$2 with "AND" with NOP) not ("AND" near3 NOP)	US-PGPUB; USPAT	OR	ON	2004/02/20 09:47
S54	63	("AND" with NOP) not ("AND" near3 NOP)	US-PGPUB; USPAT	OR	ON	2004/02/20 09:48
S55	44	mask with NOP	US-PGPUB; USPAT	OR	ON	2004/02/20 09:48
S56	9	pipeline with stall with (neutral NOP instruction)	EPO	OR	ON	2004/02/20 13:35

S57	0	test$3 with (resource hardware register) with (stall idle)	EPO	OR	ON	2004/02/20 13:32
S58	0	test$3 with (resource hardware register) with (stall$3 idle)	EPO	OR	ON	2004/02/20 13:32
S59	10	test$3 with (resource hardware register) with (stall$3 idle)	JPO	OR	ON	2004/02/20 13:34
S60	6	test$3 with (resource hardware register) with (stall$3 idle)	DERWENT	OR	ON	2004/02/20 13:34
S61	0	test$3 with (resource hardware register) with (stall$3 idle)	IBM_TDB	OR	ON	2004/02/20 13:34
S62	0	nakajo-hisao.in. hisao-nakajo.in.	USPAT	OR	ON	2004/02/20 13:35
S63	9	nakajo-h$.in. hisao-n$.in.	USPAT	OR	ON	2004/02/20 13:35
S64	16	"neutral instruction"	US-PGPUB; USPAT	OR	ON	2004/07/15 16:07
S65	0	debug$4 with (read reading) with neutral	US-PGPUB; USPAT	OR	ON	2004/11/03 15:46
S66	53	neutral near2 instruction	US-PGPUB; USPAT	OR	ON	2004/11/03 15:47
S67	30	neutral near2 instruction and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/03 17:09
S68	61	read near2 instruction with modify near5 (state register) and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/03 16:05
S69	0	read near2 instruction with "not" near2 modify near5 (state register) and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/03 16:06
S70	0	read near2 instruction with "not" near2 update near5 (state register) and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2004/11/03 16:06